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APPLICATION NO.	FILING DATE		FIRST NAMED INVENTOR	ATTO	RNEY DOCKET NO.	CONFIRMATION NO.	
10/828,884 04/21/2004 Bor-Min Tseng TSM03-0763 5862 TITLE OF INVENTION: COMPENSATED LINEARITY VOLTAGE-CONTROL-CAPACITOR DEVICE BY STANDARD CMOS PROCESS							
APPLN. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE	
nonprovisional	NO	\$1400	\$300	\$0	\$1700	04/25/2007	
EXAM	INER	ART UNIT	CLASS-SUBCLASS]			
NGUYEN, KHIEM D		2823	438-379000	•			
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This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450. Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.							
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